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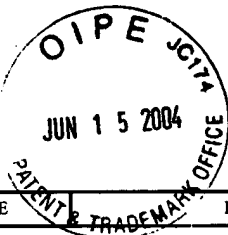
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03/22/2004

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/622,472	07/21/2003	Yasuro Matsuzaki	100021-00130	4529

TITLE OF INVENTION: SEMICONDUCTOR DEVICE, METHOD OF TESTING THE SEMICONDUCTOR DEVICE, AND SEMICONDUCTOR INTEGRATED CIRCUIT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	06/22/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
PATEL, PARESH H	2829	324-763000

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1 Arent Fox, PLLC

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FUJITSU LIMITED**Kawasaki, Japan**

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature)

(Date)

Sam Huang, Reg. No. 48,430

6/15/04

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06/17/2004 ZJUHR2 00000046 10622472

01 FC:1501

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